

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2133
Examiner: To be assigned

In Re PATENT APPLICATION Of:	Examiner: To be assigned
Applicant(s): I-Ming LIN et al.	)
Serial No. : 10/614,040	)
Filed : July 8, 2003	)
For: : Signal testing of integrated circuit chip	) <u>STATUS REQUEST</u>
Attorney Ref: TOP 296	)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. If the response to this status request is made by telephone, please ask for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

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May 27, 2005 Date

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